

S/N 10/642,702

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Vivek Jaiswal et al.	Examiner:	Chirag R. Patel
Serial No.:	10/642,702	Group Art Unit:	2141
Filed:	August 18, 2003	Docket No.:	P16507
Title:	DYNAMIC LOAD DISTRIBUTION WITHIN A SESSION INITIATION PROTOCOL NETWORK		
Assignee:	Intel Corporation		

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

This is in response to the Office Action mailed October 1, 2008. Reconsideration and allowance of the subject application, as amended, are respectfully requested.